

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-2. (Canceled)

3. (Previously Presented) A plasma display panel, comprising:

a sustaining electrode pair of a transparent conductive material provided on an upper substrate;

a plurality of barrier ribs formed on a lower substrate in a direction crossing the sustaining electrode pair; and

a display region coexisting with a non-display region, wherein a first portion of the plurality of barrier ribs are disposed substantially in the display region except for end portions thereof and a second portion of the barrier ribs are disposed partially in the non-display region along a length thereof, and wherein a width of the first portion of barrier ribs is different than a width of the second portion of barrier ribs.

4. (Previously Presented) The plasma display panel as claimed in claim 3, wherein the width of the second portion of barrier ribs is larger than the width of the first portion of barrier ribs.

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5. (Previously Presented) The plasma display panel as claimed in claim 3, wherein the width of the second portion of barrier ribs is larger than a width of each end of the sustaining electrode pair overlapping the barrier rib.

6. (Currently Amended) A plasma display panel, comprising:
a sustaining electrode pair of a transparent conductive material provided on an upper substrate;

a plurality of barrier ribs formed on a lower substrate in a direction crossing the sustaining electrode pair; and

a display region comprising a plurality of discharge cells coexisting with a non-display region provided at an outer periphery of the display region, wherein the non-display region is provided with black matrices for shutting out light.

7. (Previously Presented) The plasma display panel as claimed in claim 6, wherein the black matrices are arranged in parallel to the plurality of barrier ribs.

8. (Previously Presented) The plasma display panel as claimed in claim 7, wherein the black matrices are formed at each longitudinal end of the plurality of barrier ribs in a direction crossing the plurality of barrier ribs.

9. (Currently Amended) A plasma display panel, comprising:

- an upper substrate;
- a protective layer provided at a rear side of the upper substrate; and
- a display region comprising a plurality of discharge cells coexisting with a non--display region provided at an outer periphery of the display region, wherein the protective layer is provided only on the display region so as to cover substantially the entire display region

10. (Previously Presented) A plasma display panel, comprising:

- a lower substrate; and
- a plurality of barrier ribs formed on the lower substrate, wherein a first portion of the plurality of barrier ribs are disposed substantially in a display region of the plasma display panel except for end portions thereof and a second portion of the barrier ribs are disposed partially in a non-display region of the plasma display panel along a length thereof, and wherein a width of the first portion of barrier ribs is different than a width of the second portion of barrier ribs.

11. (Previously Presented) The plasma display panel as claimed in claim 10, wherein the width of the second portion of barrier ribs is larger than the width of the first portion of barrier ribs.

12. (Previously Presented) The plasma display panel as claimed in claim 10, further comprising:

an upper substrate; and

a sustaining electrode pair provided on the upper substrate, wherein the plurality of barrier ribs extend in a direction crossing the sustaining electrode pair.

13. (Previously Presented) The plasma display panel as claimed in claim 12, wherein the width of the second portion of barrier ribs is larger than a width of each end of the sustaining electrode pair overlapping the barrier ribs.

14. (Previously Presented) The plasma display panel as claimed in claim 13, wherein the sustaining electrode pair comprise a transparent conductive material.

15. (Currently Amended) A plasma display panel, comprising:

a display area comprising a plurality of discharge cells;

a non-display area provided at an outer periphery of the display area; and

black matrices provided on one of an upper or lower substrate in the non-display area and configured for non-transmission of light.

16. (Previously Presented) The plasma display panel as claimed in claim 15, further comprising a plurality of barrier ribs formed on the lower substrate, wherein the black matrices are arranged in parallel to the plurality of barrier ribs.

17. (Previously Presented) The plasma display panel as claimed in claim 16, wherein the black matrices are formed at each longitudinal end of the plurality of barrier ribs in a direction crossing the plurality of barrier ribs.

18. (Previously Presented) The plasma display panel as claimed in claim 16, further comprising:

an upper substrate; and
a sustaining electrode pair provided on the upper substrate, wherein the plurality of barrier ribs extend in a direction crossing the sustaining electrode pair.

19. (Previously Presented) The plasma display panel as claimed in claim 18, wherein the sustaining electrode pair comprise a transparent conductive material.

20. (Currently Amended) A plasma display panel, comprising:
a display area comprising a plurality of discharge cells; and

a non-display area provided at an outer periphery of the display area, wherein a protective layer is provided on an upper substrate only in the display region area so as to cover substantially the entire display area.

21. (Previously Presented) The plasma display panel as claimed in claim 20, wherein the protective layer is formed at a rear side of the upper substrate.

22. (Previously Presented) The plasma display panel as claimed in claim 20, further comprising black matrices provided on one of the upper substrate or a lower substrate of the plasma display panel.

23. (Previously Presented) The plasma display panel as claimed in claim 6, wherein only the non-display region is provided with black matrices for shutting out light.

24. (Previously Presented) The plasma panel as claimed in claim 15, wherein the black matrices are provided on one of the upper or lower substrate only in the non-display area.